

## STGW45HF60WD

Preliminary data

45 A, 600 V ultra fast IGBT

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# Features

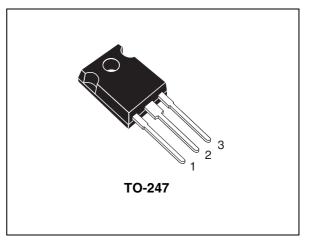
- Improved E<sub>off</sub> at elevated temperature
- Low C<sub>RES</sub> / C<sub>IES</sub> ratio (no cross-conduction susceptibility)
- Ultra fast soft recovery antiparallel diode

### Applications

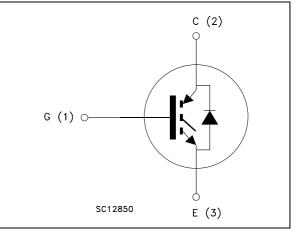
- Welding
- Induction heating
- High frequency converters
- Power factor correction

### Description

The "HF" series is based on a new planar technology concept to yield an IGBT with tighter variation of switching energy ( $E_{off}$ ) versus temperature. Suffix "W" denotes a subset of products tailored to high switching frequency operation over 100 kHz.



#### Figure 1. Internal schematic diagram



#### Table 1.Device summary

Order code	Marking	Package	Packaging
STGW45HF60WD GW45HF60WD		TO-247	Tube

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This is preliminary information on a new product now in development or undergoing evaluation. Details are subject to change without notice.

## 1 Electrical ratings

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Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V <sub>CES</sub>	Collector-emitter voltage (V <sub>GE</sub> = 0)	600	V
I <sub>C</sub> <sup>(1)</sup>	Continuous collector current at $T_C = 25 \text{ °C}$	70	Α
I <sub>C</sub> <sup>(1)</sup>	Continuous collector current at $T_C = 100 \ ^{\circ}C$	45	Α
I <sub>CP</sub> <sup>(2)</sup>	Collector current (pulsed)	TBD	Α
I <sub>CL</sub> <sup>(3)</sup>	Turn-off latching current	TBD	Α
V <sub>GE</sub>	Gate-emitter voltage	± 20	V
١ <sub>F</sub>	Diode RMS forward current at $T_C = 25 \ ^{\circ}C$	30	Α
I <sub>FSM</sub>	Surge not repetitive forward current t <sub>p</sub> = 10 ms sinusoidal	120	Α
P <sub>TOT</sub>	Total dissipation at $T_C = 25 \ ^{\circ}C$	250	W
T <sub>stg</sub>	Storage temperature	- 55 to 150	
Тj	Operating junction temperature	- 55 10 150	°C

1. Calculated according to the iterative formula:

$$I_{C}(T_{C}) = \frac{T_{j(max)} - T_{C}}{R_{thj-c} \times V_{CE(sat)(max)}(T_{j(max)}, I_{C}(T_{C}))}$$

2. Pulse width limited by max junction temperature

3.  $V_{CLAMP}$  = 80% (V<sub>CES</sub>), V<sub>GE</sub> = 15 V, R<sub>G</sub> = 10  $\Omega$ , T<sub>J</sub> = 150 °C

Symbol	Parameter	Value	Unit
D	Thermal resistance junction-case IGBT		°C/W
<sup>n</sup> thj-case	R <sub>thj-case</sub> Thermal resistance junction-case diode		°C/W
R <sub>thj-amb</sub>	Thermal resistance junction-ambient	50	°C/W



### 2 Electrical characteristics

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(T<sub>J</sub> = 25 °C unless otherwise specified)

Table 4.	Static					
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V <sub>(BR)CES</sub>	Collector-emitter breakdown voltage (V <sub>GE</sub> = 0)	I <sub>C</sub> = 1 mA	600			v
V <sub>CE(sat)</sub>	Collector-emitter saturation voltage	$V_{GE}$ = 15 V, I <sub>C</sub> = 30 A V <sub>GE</sub> = 15V, I <sub>C</sub> = 30 A,T <sub>J</sub> = 125 °C		1.9 TBD	2.5	V V
V <sub>GE(th)</sub>	Gate threshold voltage	$V_{CE} = V_{GE}$ , $I_C = 250 \mu A$	3.75		5.75	V
I <sub>CES</sub>	Collector cut-off current $(V_{GE} = 0)$	V <sub>CE</sub> = 600 V V <sub>CE</sub> = 600 V, T <sub>J</sub> = 125 °C			500 5	μA mA
I <sub>GES</sub>	Gate-emitter leakage current (V <sub>CE</sub> = 0)	V <sub>GE</sub> = ±20 V			± 100	nA
9 <sub>fs</sub>	Forward transconductance	$V_{CE} = 15 \text{ V}, \text{ I}_{C} = 30 \text{ A}$		TBD		S

#### Table 4. Static

#### Table 5. Dynamic

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
C <sub>ies</sub> C <sub>oes</sub> C <sub>res</sub>	Input capacitance Output capacitance Reverse transfer capacitance	V <sub>CE</sub> = 25 V, f = 1 MHz, V <sub>GE</sub> = 0	-	TBD TBD TBD	-	pF pF pF
Q <sub>g</sub> Q <sub>ge</sub> Q <sub>gc</sub>	Total gate charge Gate-emitter charge Gate-collector charge	$V_{CE} = 390 \text{ V}, I_{C} = 30 \text{ A},$ $V_{GE} = 15 \text{ V},$ <i>Figure 3</i>	-	TBD TBD TBD	-	nC nC nC



		/				
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t <sub>d(on)</sub> t <sub>r</sub> (di/dt) <sub>on</sub>	Turn-on delay time Current rise time Turn-on current slope	$V_{CC}$ = 390 V, I <sub>C</sub> = 30 A R <sub>G</sub> = 10 Ω, V <sub>GE</sub> = 15 V, <i>Figure 2</i>	-	TBD TBD TBD	-	ns ns A/µs
t <sub>d(on)</sub> t <sub>r</sub> (di/dt) <sub>on</sub>	Turn-on delay time Current rise time Turn-on current slope	$V_{CC} = 390 \text{ V}, I_C = 30 \text{ A}$ $R_G = 10 \Omega, V_{GE} = 15 \text{ V},$ $T_J = 125 \text{ °C}$ Figure 2	-	TBD TBD TBD	-	ns ns A/µs
t <sub>r</sub> (V <sub>off</sub> ) t <sub>d</sub> ( <sub>off</sub> ) t <sub>f</sub>	Off voltage rise time Turn-off delay time Current fall time	$V_{CC} = 390 \text{ V}, I_{C} = 30 \text{ A},$ $R_{GE} = 10 \Omega, V_{GE} = 15 \text{ V}$ <i>Figure 2</i>	-	TBD TBD TBD	-	ns ns ns
t <sub>r</sub> (V <sub>off</sub> ) t <sub>d</sub> ( <sub>off</sub> ) t <sub>f</sub>	Off voltage rise time Turn-off delay time Current fall time	$V_{CC} = 390 \text{ V}, I_C = 30 \text{ A},$ $R_{GE} = 10 \Omega, V_{GE} = 15 \text{ V},$ $T_J = 125 \text{ °C}$ <i>Figure 2</i>	-	TBD TBD TBD	-	ns ns ns

Table 6. Switching on/off (inductive load)

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 Table 7.
 Switching energy (inductive load)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
E <sub>on</sub> <sup>(1)</sup>	Turn-on switching losses	$V_{CC} = 390 \text{ V}, \text{ I}_{C} = 30 \text{ A}$		300		μJ
E <sub>off</sub>	Turn-off switching losses	$R_{G} = 10 \Omega$ , $V_{GE} = 15 V$ ,	-	250		μJ
E <sub>ts</sub>	Total switching losses	Figure 4		550		μJ
E <sub>on</sub> <sup>(1)</sup>	Turn-on switching losses	$V_{CC} = 390 \text{ V}, \text{ I}_{C} = 30 \text{ A}$		550		μJ
E <sub>off</sub>	Turn-off switching losses	$R_{G} = 10 \Omega$ , $V_{GE} = 15 V$ ,	-	500	750	μJ
E <sub>ts</sub>	Total switching losses	T <sub>J</sub> = 125 °C <i>Figure 4</i>		1050		μJ

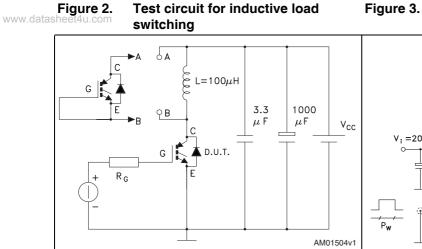
 Eon is the tun-on losses when a typical diode is used in the test circuit in *Figure 4*. If the IGBT is offered in a package with a co-pak diode, the co-pack diode is used as external diode. IGBTs & Diode are at the same temperature (25 °C and 125 °C). Eon include diode recovery energy.

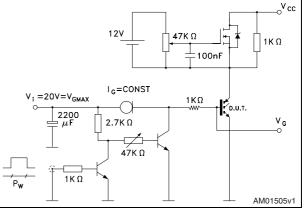
Table 8. Collector-emitter diode

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V <sub>F</sub>	Forward on-voltage	I <sub>F</sub> = 30 A I <sub>F</sub> = 30 A, T <sub>J</sub> = 125 °C	-	1.6 1.4	-	V V
t <sub>rr</sub> Q <sub>rr</sub> I <sub>rrm</sub>	Reverse recovery time Reverse recovery charge Reverse recovery current	$I_F = 30 \text{ A}, V_R = 50 \text{ V},$ di/dt = 100 A/ $\mu$ s (see Figure 5)	-	45 56 2.55	-	ns nC A
t <sub>rr</sub> Q <sub>rr</sub> I <sub>rrm</sub>	Reverse recovery time Reverse recovery charge Reverse recovery current	$\begin{split} I_{F} &= 30 \text{ A}, V_{R} = 50 \text{ V}, \\ \text{di/dt} &= 100 \text{ A} / \mu \text{s} \\ T_{J} &= 125 ^{\circ}\text{C}, \text{ (see Figure 5)} \end{split}$	-	100 290 5.8	-	ns nC A



### 3 Test circuits

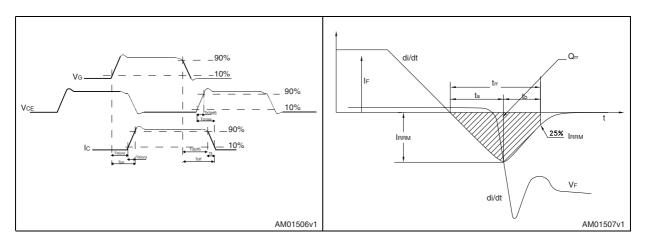




Gate charge test circuit

Figure 4. Switching waveform







### 4 Package mechanical data

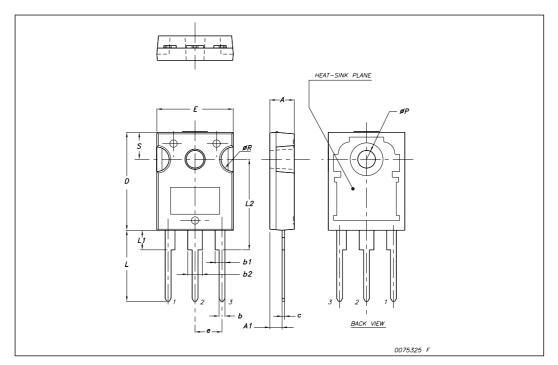
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In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.



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Dim.		mm.			
Dini.	Min.	Тур	Max.		
А	4.85		5.15		
A1	2.20		2.60		
b	1.0		1.40		
b1	2.0		2.40		
b2	3.0		3.40		
С	0.40		0.80		
D	19.85		20.15		
Е	15.45		15.75		
е		5.45			
L	14.20		14.80		
L1	3.70		4.30		
L2		18.50			
øР	3.55		3.65		
øR	4.50		5.50		
S		5.50			





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## 5 Revision history

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#### Table 9.Document revision history

Date	Revision	Changes
16-Apr-2009	1	Initial release.



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